

Understanding and Control of Electronic Properties of Nanometer-thick Dielectric Films

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【Outline of survey】

The objective of this research is to build up principles for controlling electronic structures, electric and dielectric characteristics, and furthermore hetero-interface properties of nanometer-thick insulators. This will be applicable to any cases with insulators as well as to semiconductor devices. They are poorly understood even for simple MIM capacitors, and an understanding of the interfaces with insulators such as metal/insulator, semiconductor/insulator or insulator/insulator is particularly very poor.

So, on the basis of our knowledge, skills, and understandings on those materials we have so far developed in our lab., we will further develop and extend them. We are studying about not only direct experimental quantities but also interested in indirectly obtained properties such as effective mass in the tunneling, dielectric permittivity engineering mechanism, charge redistribution at the hetero-interfaces.

【Expected results】

Concerning semiconductor device applications, results of this research should directly contribute to performance enhancements of MIS capacitors, MIS FETs for advanced CMOS, and MIM for analogue devices or flash memories. This research will be progressed from the viewpoint of engineering of tunneling effects rather than just of observing them experimentally. Furthermore, the charge redistribution at the hetero-interfaces is related to the actual device control and will be investigated from a dipole layer formation viewpoint. These results can be extended to various semiconductor applications; particularly we are very interested in insulators on Ge.

【References by the principal investigator】

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【Term of project】 FY2007—2011

【Budget allocation】 38,400,000 yen

(2007 direct cost)

【Homepage address】

<http://www.adam.t.u-tokyo.ac.jp>